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FIG.1

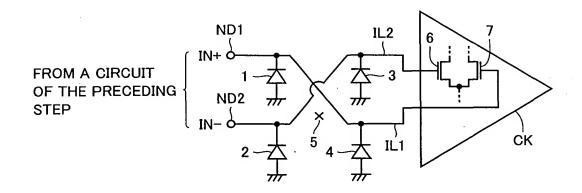
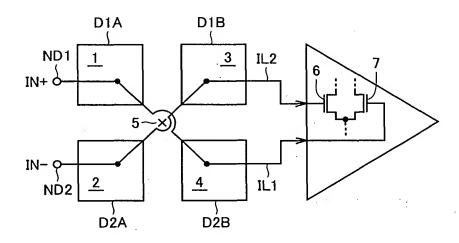


FIG.2



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FIG.3

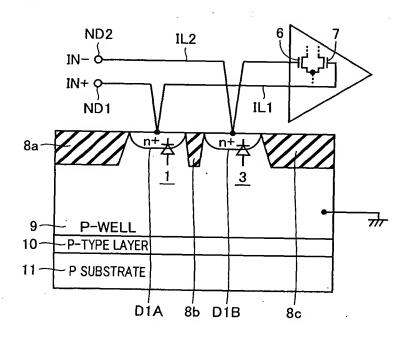
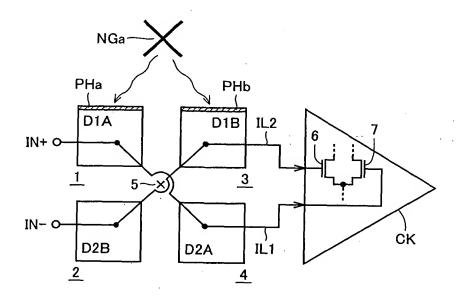


FIG.4



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FIG.5

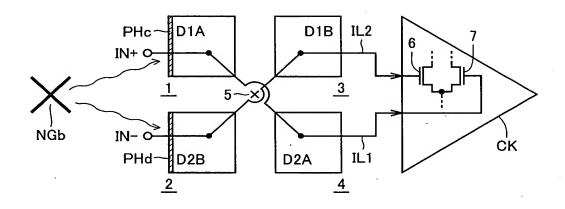
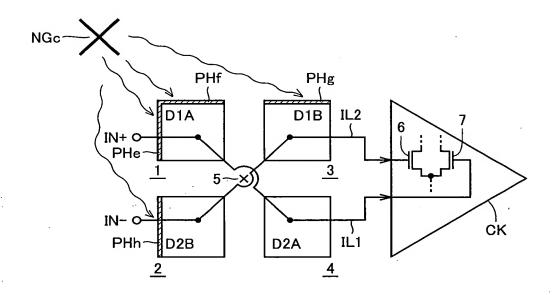


FIG.6



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FIG.7

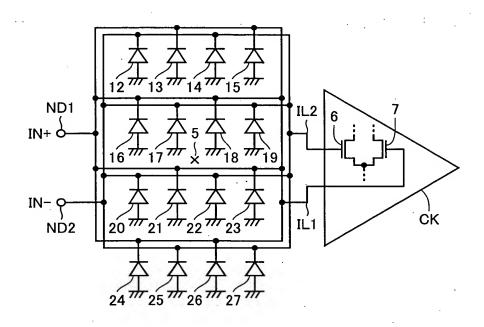
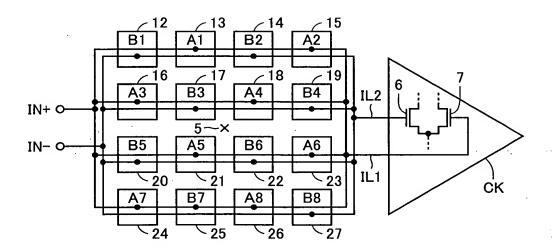


FIG.8



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FIG.9

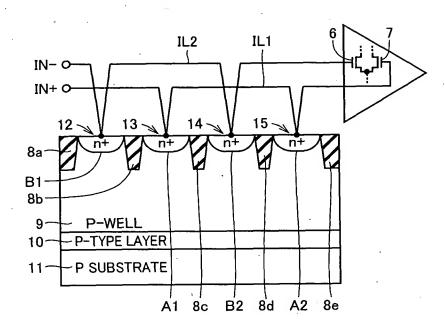
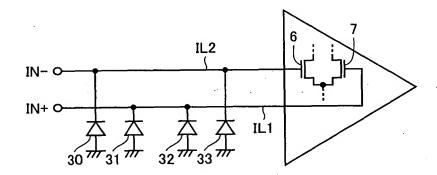


FIG.10



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FIG.11

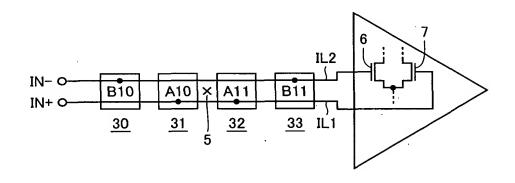
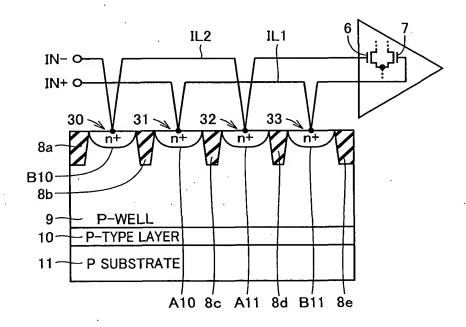


FIG.12



TITLE: SEMICONDUCTOR DEVICE INCLUDING A

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FIG.13

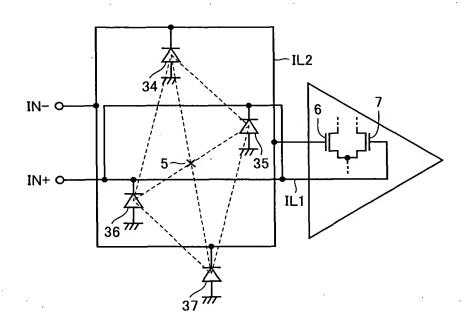
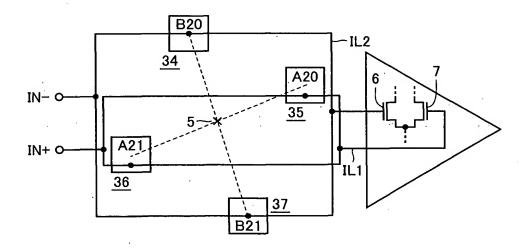


FIG.14

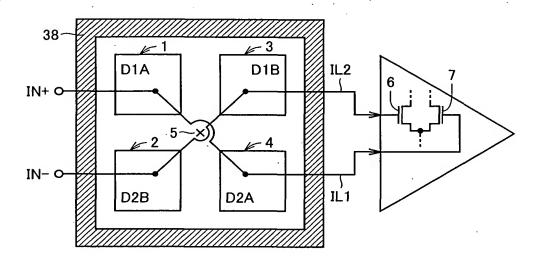


TITLE: SEMICONDUCTOR DEVICE INCLUDING A

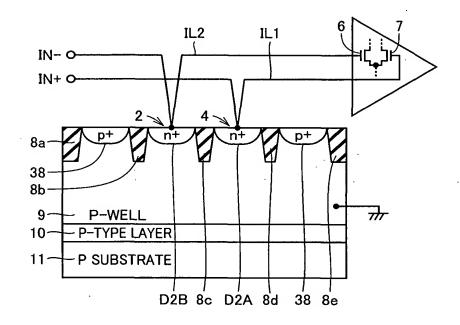
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**FIG.15** 



**FIG.16** 



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FIG.17

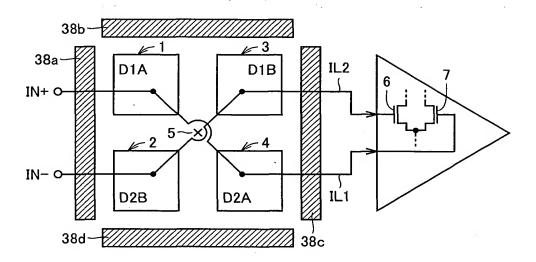
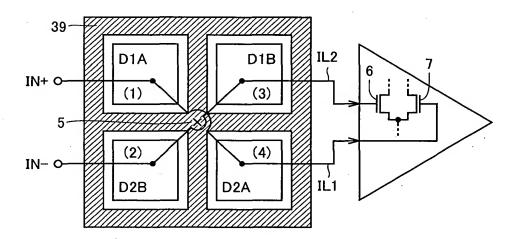


FIG.18

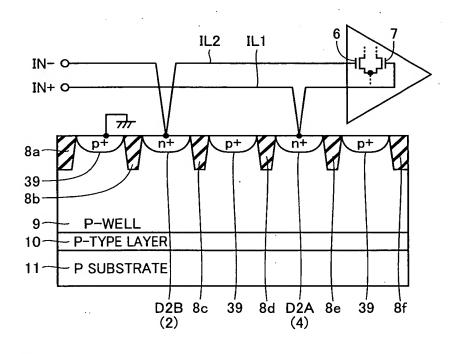


TITLE: SEMICONDUCTOR DEVICE INCLUDING A

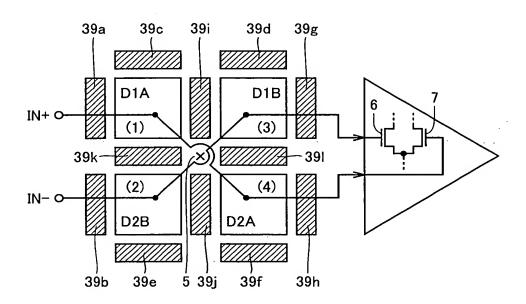
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**FIG.19** 



**FIG.20** 

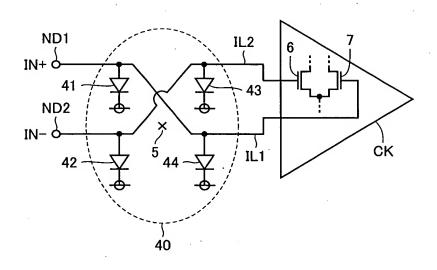


TITLE: SEMICONDUCTOR DEVICE INCLUDING A

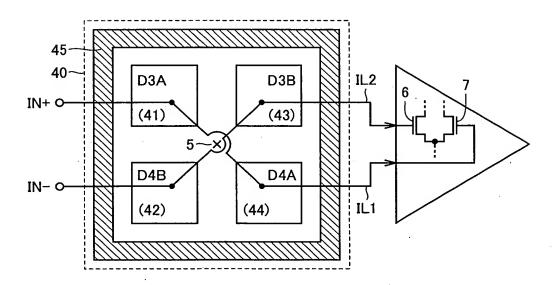
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FIG.21



**FIG.22** 

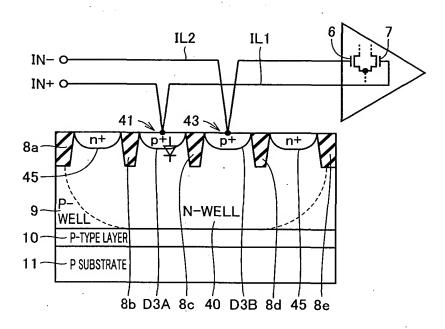


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**INVENTOR(S):** YASUO MORIMOTO ET AL.

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**FIG.23** 



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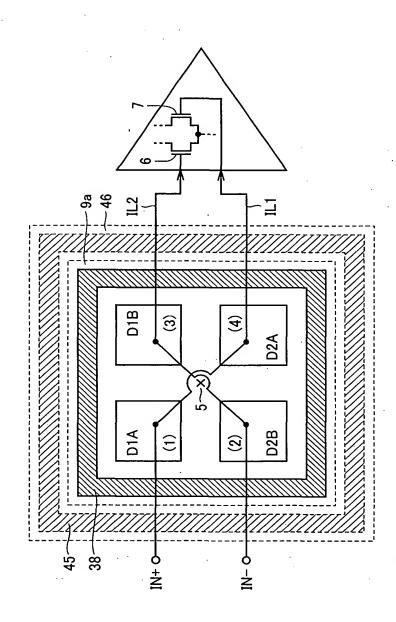


FIG.24

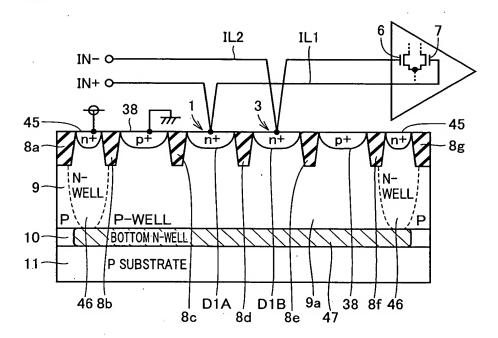
TITLE: SEMICONDUCTOR DEVICE INCLUDING A

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FIG.25

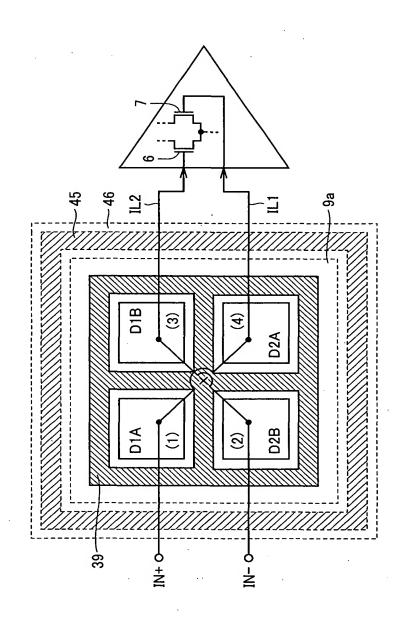


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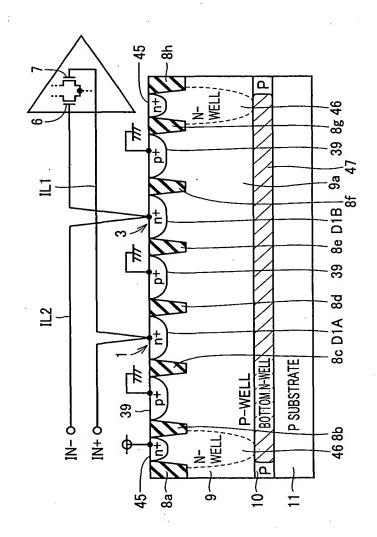


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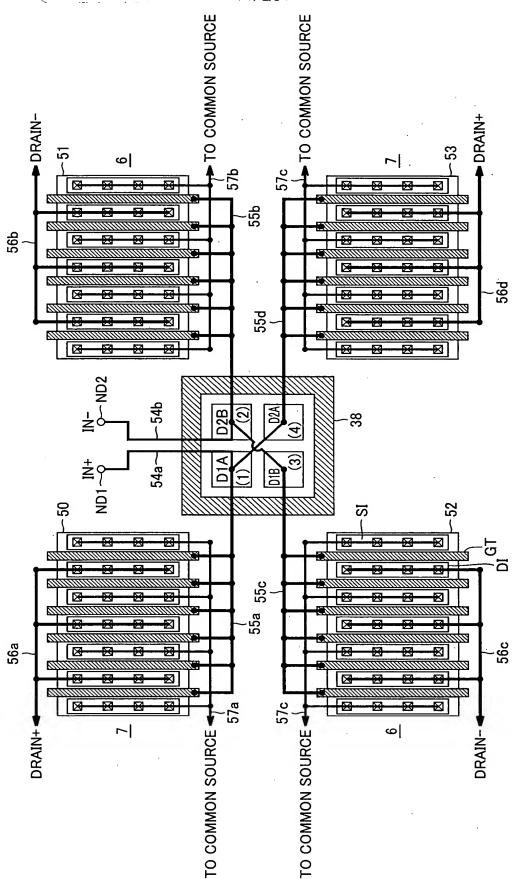
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**FIG.29** 

